

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	93	324/765.ccls. and (wafer and chip and membrane)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 15:58
L2	5	1 and (power with transfer\$4).	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 15:57
L3	202962	"324"/\$.ccls. or "714"/\$.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 15:57
L4	486	3 and (wafer and chip and membrane)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 16:06
L5	385	4 and align\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 16:00
L6	153	5 and (fabricat\$4 with process\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 16:01
L7	0	(align\$4 and (energy with transfer\$4) and wafer and chip and membrane).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/27 16:07